9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output¹, 3A to 0.7A Scaled output current



RoHS Compliant

Applications

- Industrial equipment
- Distributed power architectures
- Intermediate bus voltage applications

VIN

MODULE

GND

ON/OFF

VOUT

VS+

TRIM

Telecommunications equipment

Features

- Compliant to RoHS Directive 2011/65/EU and amended Directive (EU) 2015/863
- Compliant to REACH Directive (EC) No 1907/2006
- Compatible in a Pb-free or SnPb reflow environment (Z versions)
- Extra Wide Input voltage range (9Vdc–36Vdc)
- Output voltage programmable from 3Vdc to 18 Vdc via external resistor
- Tunable Loop[™] to optimize dynamic output voltage response
- Patent Pending AutoLimit automatic scaling of current limit with output voltage
- Output overcurrent protection (non-latching)
- Overtemperature protection
- Remote On/Off
- Remote Sense
- Small size: 20.3 mm x 11.4 mm x 8.5 mm (0.8 in x 0.45 in x 0.335 in)
- Wide operating temperature range (-40°C to 85°C)
- ANSI/UL* 62368-1 and CAN/CSA⁺ C22.2 No. 62368-1 Recognized, DIN VDE[‡] 0868-1/A11:2017 (EN62368-1:2014/A11:2017)
- ISO** 9001 and ISO 14001 certified manufacturing facilities

Description

Cin

Q1

R

'Vin+

The 9-36V ProLynxTM series of power modules are non-isolated dc-dc converters that can deliver up to 3A of output current. These modules operate over an extra wide range of input voltage ($V_{IN} = 9Vdc-36Vdc$) and provide a precisely regulated output voltage from 3Vdc to 18Vdc, programmable via an external resistor. Two new features added with this family of products are the ability to externally tune the voltage control loop and a variable current limit inversely dependent on output voltage. Other features include remote On/Off, adjustable output voltage, over current and overtemperature protection. The Tunable LoopTM, allows the user to optimize the dynamic response of the converter to match the load with reduced amount of output capacitance leading to savings on cost and PWB area and AutoLimit enables the module to deliver the max possible output power across the entire voltage range.

Vout+

Co

RTUNE

CTUNE

RTrim

* UL is a registered trademark of Underwriters Laboratories, Inc.

- ⁺ CSA is a registered trademark of Canadian Standards Association.
- * VDE is a trademark of Verband Deutscher Elektrotechniker e.V.
 ** ISO is a registered trademark of the International Organization of Standards

¹ Output range linked to input voltage range see page 24



February 17, 2021

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

| Parameter | Device | Symbol | Min | Max | Unit |
|--------------------------------------|--------|------------------|------|-----|------|
| Input Voltage | All | V _{IN} | -0.3 | 37 | Vdc |
| Continuous | | | | | |
| Operating Ambient Temperature | All | TA | -40 | 85 | °C |
| (see Thermal Considerations section) | | | | | |
| Storage Temperature | All | T _{stg} | -55 | 125 | °C |

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|--|----------------------------|--------------------------|-----|-----|-----|------------------|
| Operating Input Voltage | All | V _{IN} | 9 | _ | 36 | Vdc |
| Maximum Input Current | All | I _{IN,max} | | | 2 | Adc |
| $(V_{IN}=9V \text{ to } 36V, I_0=I_{O, max})$ | | | | | | |
| Input No Load Current | | | | | | |
| $(V_{IN} = 28V, I_0 = 0, module enabled)$ | V _{0,set} = 3Vdc | I _{IN,No load} | | 22 | | mA |
| $(V_{IN} = 28V, I_0 = 0, module enabled)$ | V _{O,set} = 18Vdc | I _{IN,No load} | | 54 | | mA |
| Input Stand-by Current | All | I _{IN,stand-by} | | 1.3 | | mA |
| (V _{IN} = 28Vdc, module disabled) | | | | | | |
| Inrush Transient | All | l²t | | 0.5 | | A ² s |
| Input Reflected Ripple Current, peak-to-peak (5Hz to 20MHz, 1 μ H source impedance; V _{IN} =0 to 36V, I ₀ = I _{Omax} ; See Test Configurations) | All | | | 25 | | тАр-р |
| Input Ripple Rejection (120Hz) | All | | -41 | -54 | -69 | dB |

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This power module can be used in a wide variety of applications, ranging from simple standalone operation to an integrated part of sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a fast-acting fuse with a maximum rating of 8 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data sheet for further information.

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Electrical Specifications (continued)

| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|---|------------------------------|----------------------|------------------|------|----------------------|-----------------------|
| Output Voltage Set-point | All | V _{O, set} | -2 | | +2 | % V _{O, set} |
| Output Voltage | All | V _{O, set} | -2.5 | | +2.5 | % V _{O, set} |
| (Over all operating input voltage, resistive load, and temperature conditions until end of life) | | | | | | |
| Adjustment Range (elected by an external resistor) (Some output voltages may not be possible depending on the input voltage – see Feature Descriptions Section) | All | Vo | 3 | | 18 | Vdc |
| Output Regulation | | | | | | |
| Line ($V_{IN}=V_{IN, min}$ to $V_{IN, max}$) | All | | | — | 0.4 | % V _{O, set} |
| Load (Io=Io, min to Io, max) | All | | | _ | 0.4 | % V _{O, set} |
| Temperature ($T_{ref}=T_{A, min}$ to $T_{A, max}$) | All | | | _ | 0.4 | % V _{O, set} |
| Remote Sense Range | All | | | | 0.5 | Vdc |
| Output Ripple and Noise on nominal output | | | | | | |
| (V_IN=V_IN, nom and I_O=I_O, min to I_O, max CO = 0.1 μF // 10 μF ceramic capacitors) | | | | | | |
| Vout=3.3V, Vin=28V | | | | | | |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | All | | | 38 | | mV _{pk-pk} |
| RMS (5Hz to 20MHz bandwidth) | All | | | 12 | | mV _{rms} |
| Vout=18V, Vin=28V | | | | | | |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | All | | | 116 | | mV _{pk-pk} |
| RMS (5Hz to 20MHz bandwidth) | All | | | 38 | | mV _{rms} |
| External Capacitance ¹ | | | | | | |
| Without the Tunable Loop™ | | | | | | |
| $\text{ESR} \ge 1 \text{ m}\Omega$ | All | C _{O, max} | 0 | — | 47 | μF |
| $\text{ESR} \ge 10 \text{ m}\Omega$ | All | C _O , max | 0 | _ | 100 | μF |
| With the Tunable Loop [™] | | | | | | |
| $ESR \geq 0.15 \ m\Omega$ | All | C _{O, max} | 0 | _ | 100 | μF |
| $\text{ESR} \ge 10 \text{ m}\Omega$ | All | C _{O, max} | 0 | _ | 2000* | μF |
| Output Current Vo=3V Vo=5V Vo=12V Vo=18V | All | lo | 0 0 0 0 | | 3 2.5 2 1.5 | Adc |
| Output Current Limit Inception (Hiccup Mode) | All | I _{O, lim} | | 200 | | % I _{o,max} |
| Output Short-Circuit Current | All | I _{O, s/c} | | 1 | | Arms |
| (V₀≤250mV) (Hiccup Mode) | | | | | | |
| Efficiency (I ₀ =I _{0, max} , V ₀ = V _{0,set}) | | | | Ī | | |
| V _{IN} = 12Vdc, T _A =25°C | V _{O, set} = 3.3Vdc | η | | 93.2 | | % |
| V _{IN} = 12Vdc, T _A =25°C | V _{O, set} = 5Vdc | η | | 95.5 | | % |
| V _{IN} = 28Vdc, T _A =25°C | V _{O,set} = 12Vdc | η | | 96.0 | | % |
| V _{IN} = 28Vdc, T _A =25°C | V _{O,set} = 18Vdc | η | | 97.0 | | % |
| Switching Frequency | All | fsw | _ | 300 | | kHz |

¹Depending on Input and Output Voltage, external capacitors require using the new Tunable Loop[™] feature to ensure that the module is stable as well as getting the best transient response. See the Tunable Loop[™] section for details.

* Larger values may be possible at specific output voltages. Please consult your GE Technical representative for additional details.

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

General Specifications

| Parameter | Min | Тур | Max | Unit |
|---|-----|-------------|-----|---------|
| Calculated MTBF (I_0=0.8I_{O, max,} T_A=40°C) Telcordia Issue 2 Method 1 Case 3 | | 18,014,158 | | Hours |
| Weight | | 3.5 (0.123) | _ | g (oz.) |

Feature Specifications

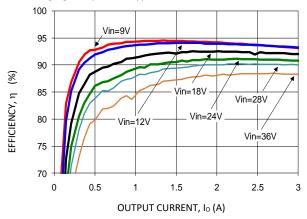
Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

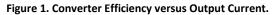
| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|--|--------|------------------|------|-----|------|-----------------------|
| On/Off Signal Interface | | | | | | |
| $(V_{IN}=V_{IN, min}$ to $V_{IN, max}$; open collector or equivalent, | | | | | | |
| Signal referenced to GND) | | | | | | |
| Device is with suffix "4" – Positive Logic (See Ordering Information) | | | | | | |
| Logic High (Module ON) | | | | | | |
| Input High Current | All | Ін | - | — | 160 | μΑ |
| Input High Voltage | All | Vін | 4.2 | _ | 12 | V |
| Logic Low (Module OFF) | | | | | | |
| Input Low Current | All | IIL | _ | _ | 0.5 | mA |
| Input Low Voltage | All | VIL | -0.3 | _ | 3.3 | V |
| Device Code with no suffix – Negative Logic (See Ordering Information) | | | | | | |
| Logic High (Module OFF) | All | lu. | | | 3 | mA |
| Input High Current Input High Voltage | All | Iн Viн | 1.5 | | 36 | Vdc |
| Logic Low (Module ON) | | VIII | 1.5 | | 50 | Vuc |
| Input low Current | All | lı. | _ | _ | 220 | μΑ |
| Input Low Voltage | All | VIL | -0.2 | _ | 1 | Vdc |
| Turn-On Delay and Rise Times | | | | | | |
| $(V_{IN}=V_{IN, nom}, I_O=I_{O, max}, V_O$ to within ±1% of steady state) | | | | | | |
| Case 1: On/Off input is enabled and then input power is applied (delay from instant at which $V_{IN} = V_{IN, min}$ until $V_0 =$ 10% of $V_{0, set}$) | All | Tdelay | _ | 12 | _ | msec |
| Case 2: Input power is applied for at least one second and then the On/Off input is enabled (delay from instant at which Von/Off is enabled until $V_0 = 10\%$ of $V_{0, set}$) | All | Tdelay | _ | 11 | _ | msec |
| Output voltage Rise time (time for V₀ to rise from 10% of Vo, set to 90% of Vo, set) | All | Trise | - | 19 | _ | msec |
| Output voltage overshoot (T _A = 25°C | | | | | 3 | % V _{O, set} |
| $V_{\rm IN} = V_{\rm IN,min}$ to $V_{\rm IN,max}, I_{\rm O} = I_{\rm O,min}$ to $I_{\rm O,max})$ | | | | | | |
| With or without maximum external capacitance | | | | | | |
| Over Temperature Protection | All | T _{ref} | | 130 | | °C |
| (See Thermal Considerations section) | | | | | | |
| Input Undervoltage Lockout | | | | | | |
| Turn-on Threshold | All | | | | 8.45 | Vdc |
| Turn-off Threshold | All | | | | 8.25 | Vdc |
| Hysteresis | All | | | 0.2 | | Vdc |

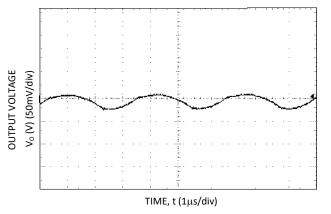
9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Characteristic Curves

The following figures provide typical characteristics for the 9-36V ProTLynx[™] 3A at 3.3Vo and at 25°C.







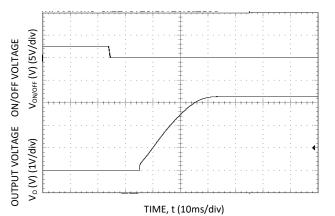
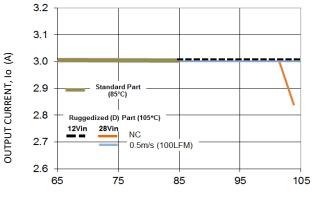


Figure 3. Typical output ripple and noise (VIN = 18V, Io = Io,max).

Figure 5. Typical Start-up Using On/Off Voltage (Io = Io,max).



AMBIENT TEMPERATURE, TA ^OC

Figure 2. Derating Output Current versus Ambient Temperature

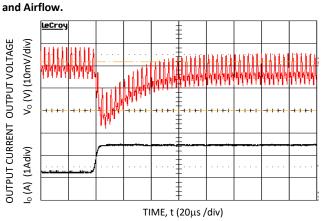


Figure 4. Transient Response to Dynamic Load Change from 50% to 100% at 28Vin, Cext - 10uF ceramic + 330uF polymer, CTune=5600pF & RTune=261 Ω

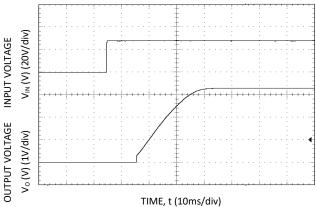


Figure 6. Typical Start-up Using Input Voltage (VIN = 28V, Io = Io,max).

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Characteristic Curves

The following figures provide typical characteristics for the 9-36V ProLynx[™] 3A at 5Vo and at 25°C.

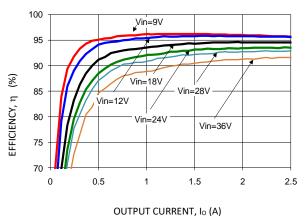


Figure 7. Converter Efficiency versus Output Current.

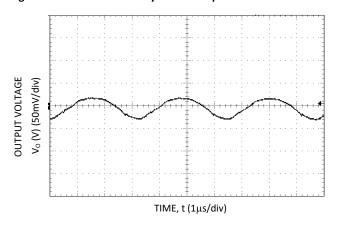


Figure 9. Typical output ripple and noise (VIN = 18V, Io = Io,max).

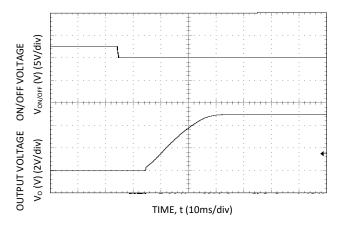
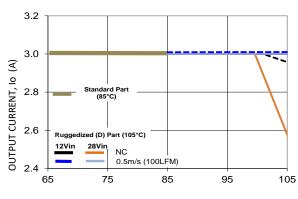
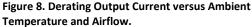


Figure 11. Typical Start-up Using On/Off Voltage (Io = Io,max).



AMBIENT TEMPERATURE, TA ^OC



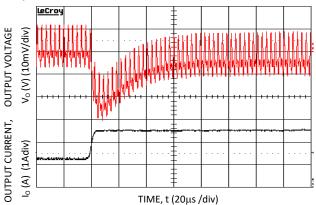


Figure 10. Transient Response to Dynamic Load Change from 50% to 100% at 28Vin, Cext - 10uF ceramic + 330uF polymer, CTune=5600pF & RTune=261 Ω

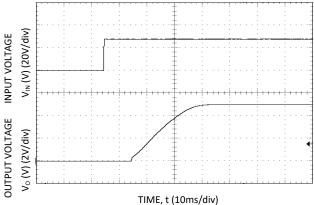
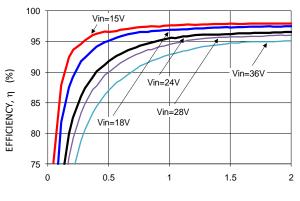


Figure 12. Typical Start-up Using Input Voltage ($V_{IN} = 28V$, $I_0 = I_{0,max}$).

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Characteristic Curves

The following figures provide typical characteristics for the 9-36V ProLynx[™] 3A at 12Vo and at 25°C.



OUTPUT CURRENT, I_o (A)



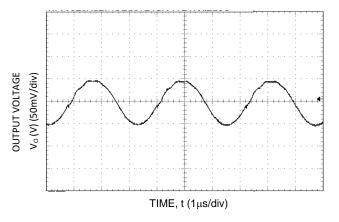


Figure 15. Typical output ripple and noise (VIN = 28V, Io = Io,max).

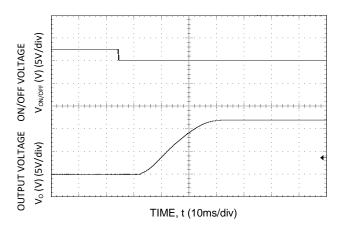
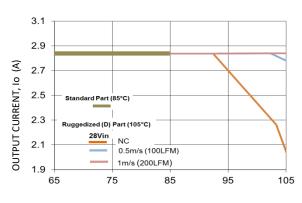
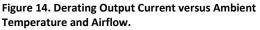


Figure 17. Typical Start-up Using On/Off Voltage (Io = Io,max).



AMBIENT TEMPERATURE, TA ^OC



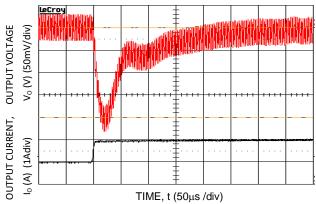


Figure 16. Transient Response to Dynamic Load Change from 50% to 100% at 28Vin, Cext - 3x10uF ceramic, CTune=47pF & RTune= 332Ω

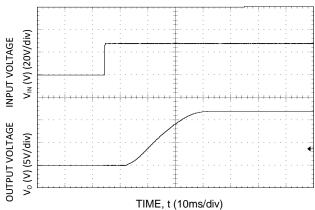
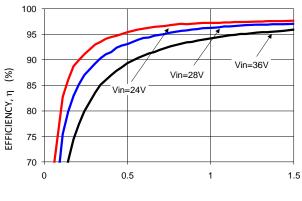


Figure 18. Typical Start-up Using Input Voltage (VIN = 28V, Io = Io,max).

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

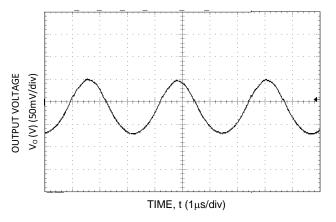
Characteristic Curves

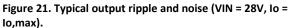
The following figures provide typical characteristics for the 9-36V ProLynx[™] 3A at 18Vo and at 25°C.



OUTPUT CURRENT, Io (A)







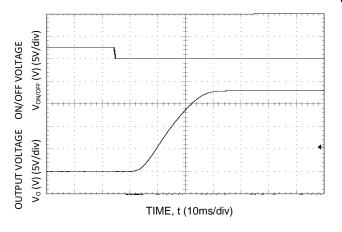
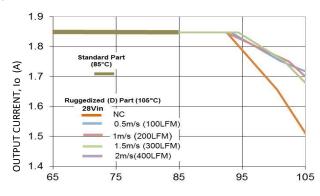
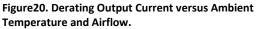


Figure 23. Typical Start-up Using On/Off Voltage (Io = Io,max).



AMBIENT TEMPERATURE, $T_A\,{}^O\!C$



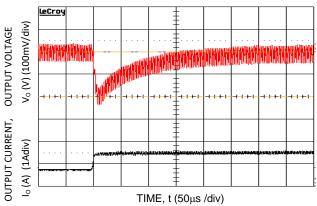


Figure 22. Transient Response to Dynamic Load Change from 50% to 100% at 28Vin, Cext - 1x10uF ceramic, CTune=open & RTune=open

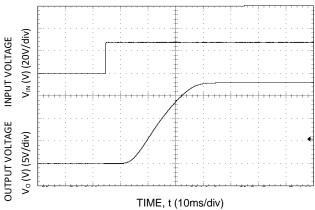
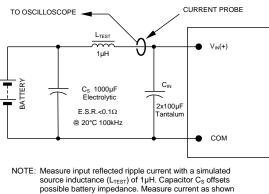


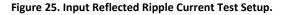
Figure 24. Typical Start-up Using Input Voltage (VIN = 28V, Io = Io,max).

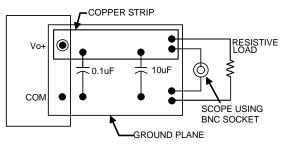
9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc -24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Test Configurations



above.





NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.



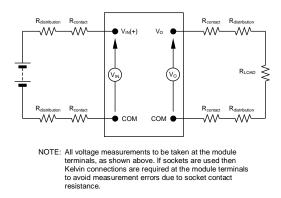


Figure 27 Output Voltage and Efficiency Test Setup.

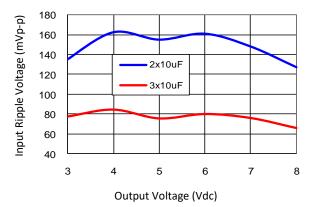
Efficiency
$$\eta = \frac{V_0. I_0}{V_{IN} I_{IN}} \times 100 \%$$

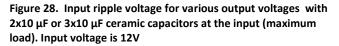
Design Considerations

Input Filtering

The 9-36V ProLynx[™] module should be connected to a low acimpedance source. A highly inductive source can affect the stability of the module. An input capacitance must be placed directly adjacent to the input pin of the module, to minimize input ripple voltage and ensure module stability.

To minimize input voltage ripple, ceramic capacitors are recommended at the input of the module. Figure 28 shows the input ripple voltage for various output voltages at maximum load current with 2x10 μ F or 3x10 μ F ceramic capacitors and an input of 12V while Fig. 29 shows the input ripple for an input voltage of 28V.





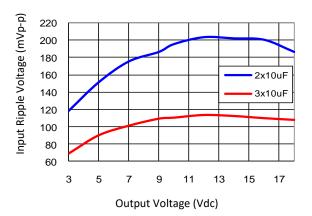


Figure 29. Input ripple voltage for various output voltages with 2x10 µF or 3x10 µF ceramic capacitors at the input (maximum load). Input voltage is 28V

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Output Filtering

The 9-36V ProLynxTM modules are designed for low output ripple voltage and will meet the maximum output ripple specification with 0.1 μ F ceramic and 10 μ F ceramic capacitors at the output of the module. However, additional output filtering may be required by the system designer for a number of reasons. First, there may be a need to further reduce the output ripple and noise of the module. Second, the dynamic response characteristics may need to be customized to a particular load step change.

To reduce the output ripple and improve the dynamic response to a step load change, additional capacitance at the output can be used. Low ESR polymer and ceramic capacitors are recommended to improve the dynamic response of the module. Figures 6 and 7 provides output ripple information for different external capacitance values at various Vo and for full load currents. For stable operation of the module, limit the capacitance to less than the maximum output capacitance as specified in the electrical specification table. Optimal performance of the module can be achieved by using the Tunable Loop[™] feature described later in this data sheet.

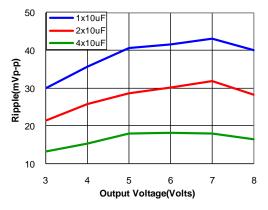


Figure 30 Output ripple voltage for various output voltages with external 1x10 μ F, 2x10 μ F or 4x10 μ F ceramic capacitors at the output (max load). Input voltage is 12V

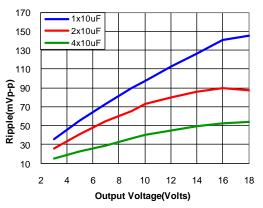


Figure 31 Output ripple voltage for various output voltages with external 1x10 μ F, 2x10 μ F or 4x10 μ F ceramic capacitors at the output (max load). Input voltage is 28V

Safety Considerations

For safety agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards, i.e., UL ANSI/UL 62368-1 and CAN/CSA C22.2 No. 62368-1 Recognized, DIN VDE 0868-1/A11:2017 (EN62368-1:2014/A11:2017)

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV) or ES1, the input must meet SELV/ES1 requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a fast-acting fuse with a maximum rating of 8A in the positive input lead.

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Feature Descriptions

Remote Enable

The 9-36V ProLynx[™] modules feature an On/Off pin for remote On/Off operation. Two On/Off logic options are available. In the Positive Logic On/Off option, (device code suffix "4" – see Ordering Information), the module turns ON during a logic High on the On/Off pin and turns OFF during a logic Low. With the Negative Logic On/Off option, (no device code suffix, see Ordering Information), the module turns OFF during logic High and ON during logic Low. The On/Off signal is always referenced to ground.

For positive logic modules, the circuit configuration for using the On/Off pin is shown in Figure 32. When the external transistor Q1 is in the OFF state, the ON/OFF pin is pulled high and transistor Q2 is OFF leading to Q3 also being OFF which turns the module ON. The external resistor R_{pullup} (100k recommended) must be sized so that $V_{ON/OFF}$ is never more than 12V when Q1 is OFF. In particular, if V_{pullup} must be large enough so that $V_{ON/OFF}$ is never more than 12V. If the On/Off pin is left floating the module will be in the ON state.

For negative logic On/Off modules, the circuit configuration is shown in Fig. 33. When the external transistor Q1 is in the ON state, the ON/OFF pin is pulled low causing transistor Q2 to be OFF and the module to be turned ON. To turn the module OFF, Q1 is turned OFF, causing the ON/OFF pin to be pulled high turing Q2 ON and the module to be turned OFF. Leaving the On/Off pin floating will leave the module in an OFF state.

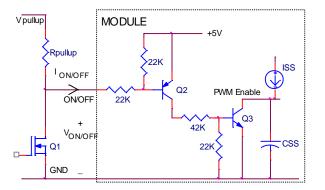


Figure 32. Circuit configuration for using positive On/Off logic.

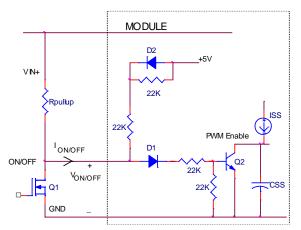


Figure 33. Circuit configuration for using negative On/Off logic.

Overcurrent Protection

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting continuously. At the point of current-limit inception, the unit enters hiccup mode. The unit operates normally once the output current is brought back into its specified range. The 9-36V ProLynx modules employ an innovative, patent pending, 'AutoLimit' capability. This results in automatic scaling of current limit with output voltage through an inverse relationship of the current limit threshold with the output voltage. This feature shown graphically in Fig. 34, allows higher output currents to be drawn from the module at lower output voltages thereby optimizing the power delivery capability of the module.

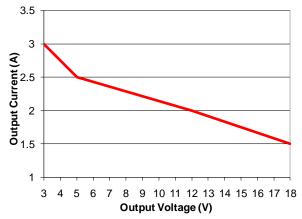


Figure 34. Graph showing maximum output current capability at different output voltages.

Over Temperature Protection

To provide protection in a fault condition, the unit is equipped with a thermal shutdown circuit. The unit will shutdown if the overtemperature threshold of 130°C is exceeded at the thermal reference point $T_{ref.}$ The thermal shutdown is not intended as a guarantee that the unit will survive temperatures beyond its rating. Once the unit goes into thermal shutdown it will then wait to cool before attempting to restart.

GE

9-36V ProLynx[™] 3A: Non-Isolated DC-DC Power Modules

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Input Undervoltage Lockout

At input voltages below the input undervoltage lockout limit, the module operation is disabled. The module will begin to operate at an input voltage above the under voltage lockout turn-on threshold.

Output Voltage Programming

The output voltage of the 9-36V ProLynx[™] module can be programmed to any voltage from 3Vdc to 18Vdc by connecting a resistor between the Trim and GND pins of the module. Certain restrictions apply on the output voltage set point depending on the input voltage. These are shown in the Output Voltage vs. Input Voltage Set Point Area plot in Fig. 9. Without an external resistor between Trim and GND pins, the output of the module will be 0.7Vdc. To calculate the value of the trim resistor, *Rtrim* for a desired output voltage, use the following equation:

$$Rtrim = \left\lceil \frac{70}{(Vo - 0.7)} \right\rceil k\Omega$$

Rtrim is the external resistor in $k\Omega,$ and $\ensuremath{\textit{Vo}}$ is the desired output voltage.

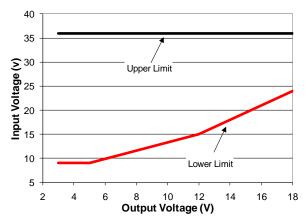


Figure 35. Output Voltage vs. Input Voltage Set Point Area plot showing limits where the output voltage can be set for different input voltages.

Table 1 provides Rtrim values required for some common output voltages.

| Table [•] | 1 |
|--------------------|---|
|--------------------|---|

| Vo, set (V) | <i>Rtrim (</i> KΩ) |
|-------------|--------------------|
| 3.3 | 26.92 |
| 5 | 16.27 |
| 6 | 13.2 |
| 9 | 8.43 |
| 12 | 6.19 |
| 15 | 4.89 |
| 18 | 4.04 |

By using a $\pm 0.5\%$ tolerance trim resistor with a TC of ± 100 ppm, a set point tolerance of $\pm 1.5\%$ can be achieved as specified in the electrical specification.

Remote Sense

The 9-36V ProLynx[™] power modules have a Remote Sense feature to minimize the effects of distribution losses by regulating the voltage between the VS+ and Vo pin. The voltage between the VS+ pin and Vo pin will not exceed 0.5V.

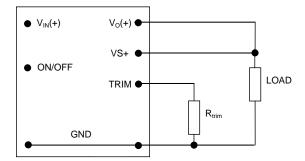


Figure 36. Circuit configuration for programming output voltage using an external resistor.

Voltage Margining

Output voltage margining can be implemented in the 9-36V ProLynx[™] modules by connecting a resistor, R_{margin-up}, from the Trim pin to the ground pin for margining-up the output voltage and by connecting a resistor, R_{margin-down}, from the Trim pin to output pin for margining-down. Figure 37 shows the circuit configuration for output voltage margining. The Lynx Programming Tool, available at <u>Go.ABB/Industrial</u> under the Design Tools section, also calculates the values of R_{margin-up} and R_{margin-down} for a specific output voltage and % margin Please consult your local GE technical representative for additional details.

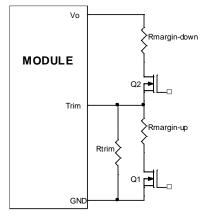


Figure 37. Circuit Configuration for margining Output voltage

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Tunable Loop[™]

The 9-36V ProLynx[™] modules have a new feature that optimizes transient response of the module called Tunable Loop[™].

External capacitors are usually added to the output of the module for two reasons: to reduce output ripple and noise (see Figures 30 and 31) and to reduce output voltage deviations from the steady-state value in the presence of dynamic load current changes. Adding external capacitance however affects the voltage control loop of the module, typically causing the loop to slow down with sluggish response. Larger values of external capacitance could also cause the module to become unstable.

The Tunable Loop[™] allows the user to externally adjust the voltage control loop to match the filter network connected to the output of the module. The Tunable Loop[™] is implemented by connecting a series R-C between the VS+ and TRIM pins of the module, as shown in Fig. 38. This R-C allows the user to externally adjust the voltage loop feedback compensation of the module.

Recommended values of R_{TUNE} and C_{TUNE} for different output capacitor combinations are given in Tables 2, 3 and 4. Tables 2 and 3 show recommended values of R_{TUNE} and C_{TUNE} for different values of ceramic output capacitors up to 100μ F that might be needed for an application to meet output ripple and noise requirements. Selecting R_{TUNE} and C_{TUNE} according to Tables 2 and 3 will ensure stable operation of the module

In applications with tight output voltage limits in the presence of dynamic current loading, additional output capacitance will be required. Table 4 lists recommended values of R_{TUNE} and C_{TUNE} in order to meet 2% output voltage deviation limits for some common output voltages in the presence of a 50% of full load step change with an input voltage of 12 or 28V.

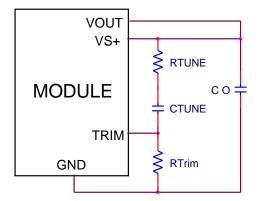


Figure. 38. Circuit diagram showing connection of R_{TUME} and C_{TUNE} to tune the control loop of the module.

Please contact your GE technical representative to obtain more details of this feature as well as for guidelines on how to select the right value of external R-C to tune the module for best transient performance and stable operation for other output capacitance values or input voltages other than 12V/28V.

Table 2. General recommended values of of R_{TUNE} and C_{TUNE} for Vin=12V and various external ceramic capacitor combinations. Vo=5V

| Со | 1x10µF | 1x22μF | 2x22μF | 4x22μF | 6x22μF |
|-------------------|--------|--------|--------|--------|--------|
| R _{TUNE} | 330 | 270 | 220 | 180 | 180 |
| CTUNE | 330pF | 680pF | 1500pF | 2700pF | 3300pF |

Table 3. General recommended values of of R_{TUNE} and C_{TUNE} for Vin=28V and various external ceramic capacitor combinations.

Vo=5V

| С | 0 | 1x10μF | 1x22μF | 2x22μF | 4x22μF | 6x22μF |
|----|-----|--------|--------|--------|--------|--------|
| RT | UNE | Open | 330 | 270 | 220 | 180 |
| Ст | UNE | Open | 150pF | 470pF | 1000pF | 1500p |

Vo=12V

| Со | 1x10μF | 1x22μF | 2x22μF | 4x22μF | 6x22μF |
|-------|--------|--------|--------|--------|--------|
| RTUNE | Open | 330 | 270 | 270 | 220 |
| CTUNE | Open | 220p | 330p | 680p | 1200p |

Table 4. Recommended values of R_{TUNE} and C_{TUNE} to obtain transient deviation of 2% of Vout for a 50% of full load step

| Vin | 12 | 2V | | 28V | | | | |
|-------------------|------------------|------------------|------------------|------------------|--------|--------|--|--|
| Vo | 3.3V | 5V | 3.3V | 5V | 12V | 18V | | |
| Δ١ | 1.5A | 1.25A | 1.5A | 1.25A | 1A | 0.75A | | |
| Co | 1x330μF OsCon | 1x330μF OsCon | 1x330μF OsCon | 1x330μF OsCon | 2x22μF | 1x22µF | | |
| RTUNE | 220 | 220 | 270 | 270 | 330 | Open | | |
| C _{TUNE} | 15nF | 15nF | 5600pF | 5600pF | 47pF | Open | | |
| ΔV | 26mV | 22mV | 24mV | 20mV | 223mV | 193mV | | |

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Thermal Considerations

Power modules operate in a variety of thermal environments; however, sufficient cooling should always be provided to help ensure reliable operation.

Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel. The test set-up is shown in Figure 39. The preferred airflow direction for the module is in Figure 40. The derating data applies to airflow in either direction of the module's short axis.

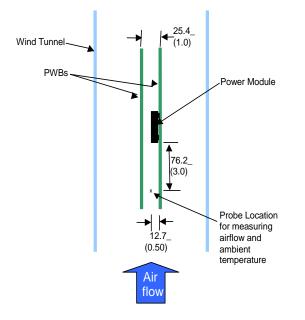


Figure 39. Thermal Test Setup.

The thermal reference points, T_{ref} used in the specifications are also shown in Figure 40. For reliable operation the temperatures at these points should not exceed 130°C. The output power of the module should not exceed the rated power of the module (Vo,set x lo,max).

Please refer to the Application Note "Thermal Characterization Process For Open-Frame Board-Mounted Power Modules" for a detailed discussion of thermal aspects including maximum device temperatures.

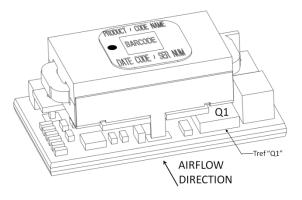


Figure 40. Preferred airflow direction and location of hot-spot of the module (Tref).

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Shock and Vibration

The APXW003 modules are designed to withstand elevated levels of shock and vibration to be able to operate in harsh environments. The ruggedized modules have been successfully tested to the following conditions:

Non operating random vibration:

Random vibration tests conducted at 25C, 10 to 2000Hz, for 30 minutes each level, starting from 30Grms (Z axis) and up to 50Grms (Z axis). The units were then subjected to two more tests of 50Grms at 30 minutes each for a total of 90 minutes.

Operating shock to 40G per Mil Std. 810G, Method 516.4 Procedure I:

The modules were tested in opposing directions along each of three orthogonal axes, with waveform and amplitude of the shock impulse characteristics as follows:

All shocks were half sine pulses, 11 milliseconds (ms) in duration in all 3 axes.

Units were tested to the Functional Shock Test of MIL-STD-810, Method 516.4, Procedure I - Figure 516.4-4. A shock magnitude of 40G was utilized. The operational units were subjected to three shocks in each direction along three axes for a total of eighteen shocks.

Operating vibration per Mil Std 810G, Method 514.5 Procedure I:

The APXW003 modules are designed and tested to vibration levels as outlined in MIL-STD-810G, Method 514.5, and Procedure 1, using the Power Spectral Density (PSD) profiles as shown in Table 1 and Table 2 for all axes. Full compliance with performance specifications was required during the performance test. No damage was allowed to the module and full compliance to performance specifications was required when the endurance environment was removed. The module was tested per MIL-STD-810, Method 514.5, Procedure I, for functional (performance) and endurance random vibration using the performance and endurance levels shown in Table 5 and Table 6 for all axes. The performance test has been split, with one half accomplished before the endurance test and one half after the endurance test (in each axis). The duration of the performance test was at least 16 minutes total per axis and at least 120 minutes total per axis for the endurance test. The endurance test period was 2 hours minimum per axis.

Table 5: Performance Vibration Qualification - All Axes

| Frequency (Hz) | PSD Level (G2/Hz) | Frequency (Hz) | PSD Level (G2/Hz) | Frequency (Hz) | PSD Level (G2/Hz) |
|----------------|----------------------|----------------|----------------------|----------------|----------------------|
| 10 | 1.14E-03 | 170 | 2.54E-03 | 690 | 1.03E-03 |
| 30 | 5.96E-03 | 230 | 3.70E-03 | 800 | 7.29E-03 |
| 40 | 9.53E-04 | 290 | 7.99E-04 | 890 | 1.00E-03 |
| 50 | 2.08E-03 | 340 | 1.12E-02 | 1070 | 2.67E-03 |
| 90 | 2.08E-03 | 370 | 1.12E-02 | 1240 | 1.08E-03 |
| 110 | 7.05E-04 | 430 | 8.84E-04 | 1550 | 2.54E-03 |
| 130 | 5.00E-03 | 490 | 1.54E-03 | 1780 | 2.88E-03 |
| 140 | 8.20E-04 | 560 | 5.62E-04 | 2000 | 5.62E-04 |

Table 6: Endurance Vibration Qualification - All Axes

| Frequency (Hz) | PSD Level (G2/Hz) | Frequency (Hz) | PSD Level (G2/Hz) | Frequency (Hz) | PSD Level (G2/Hz) |
|----------------|----------------------|----------------|----------------------|----------------|----------------------|
| 10 | 0.00803 | 170 | 0.01795 | 690 | 0.00727 |
| 30 | 0.04216 | 230 | 0.02616 | 800 | 0.05155 |
| 40 | 0.00674 | 290 | 0.00565 | 890 | 0.00709 |
| 50 | 0.01468 | 340 | 0.07901 | 1070 | 0.01887 |
| 90 | 0.01468 | 370 | 0.07901 | 1240 | 0.00764 |
| 110 | 0.00498 | 430 | 0.00625 | 1550 | 0.01795 |
| 130 | 0.03536 | 490 | 0.01086 | 1780 | 0.02035 |
| 140 | 0.0058 | 560 | 0.00398 | 2000 | 0.00398 |

GE

9-36V ProLynx[™] 3A: Non-Isolated DC-DC Power Modules

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Example Application Circuit

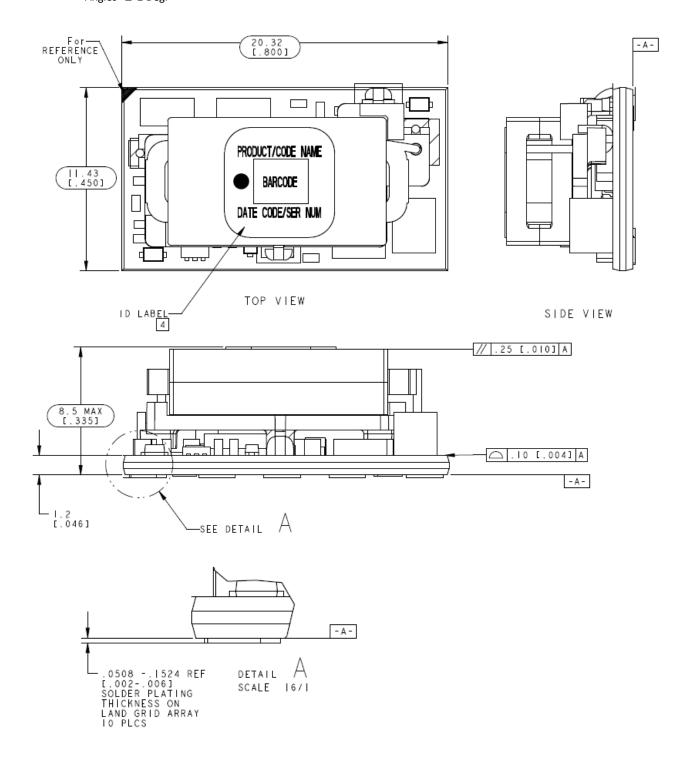
Requirements: Vin: 28V Vout: 12V lout: 1.5A max., worst case load transient is from 1A to 1.5A ∆Vout: 1.5% of Vout (180mV) for worst case load transient Vin, ripple 1.5% of Vin (420mV, p-p) Vout+ Vin+ VIN VOUT VS+ RTUNE 100K MODULE CI3 CO3 CO2 CO1 CI2 📥 CI1 CTUNE **ON/OFF** TRIM Q1 **RTrim GND**

| CI1 | 1 x 0.01µF/50V, 0603 ceramic capacitor |
|-------|--|
| CI2 | 2 x 10μF/50V ceramic capacitor (e.g. Murata GRM32ER71H106K) |
| CI3 | 47µF/63V bulk electrolytic |
| CO1 | 1 x 0.01µF/25V, 0306 ceramic capacitor (e.g. Murata LLL185R71E103MA01L)) |
| CO2 | 2 x 10μF/25V ceramic capacitor (e.g. Murata GCM32ER71E106KA42) |
| CO3 | NA |
| CTune | 47pF ceramic capacitor (can be 1206, 0805 or 0603 size) |
| RTune | 332 ohms SMT resistor (can be 1206, 0805 or 0603 size) |
| RTrim | 6.19k Ω resistor |
| | |

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Mechanical Outline

Dimensions are in millimeters and (inches). Tolerances: x.x mm \pm 0.5 mm (x.xx in. \pm 0.02 in.) [unless otherwise indicated] x.xx mm \pm 0.25 mm (x.xxx in \pm 0.010 in.) Angles \pm 2 Deg.



GE

9-36V ProLynx[™] 3A: Non-Isolated DC-DC Power Modules

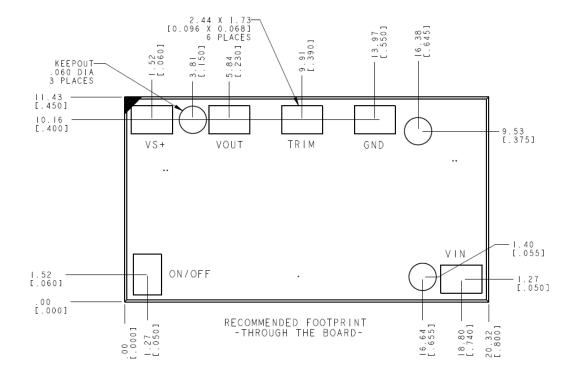
9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Recommended Pad Layout

Dimensions are in millimeters and (inches).

Tolerances: x.x mm \pm 0.5 mm (x.xx in. \pm 0.02 in.) [unless otherwise indicated]

x.xx mm \pm 0.25 mm (x.xxx in \pm 0.010 in.)



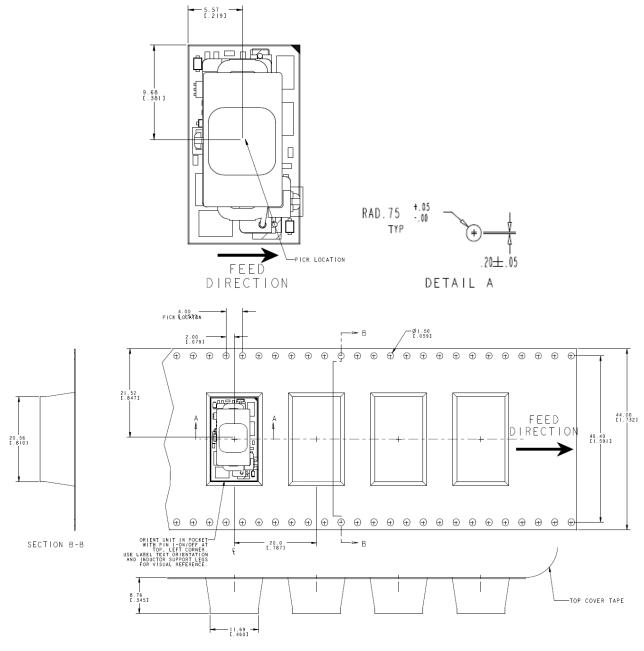
| PIN | Pin | Description | | |
|-----|--------|----------------------------|--|--|
| 1 | ON/OFF | Remote On/Off control | | |
| 2 | Vin | Positive power input | | |
| 3 | GND | Common ground | | |
| 4 | TRIM | Output voltage programming | | |
| 5 | VOUT | Positive power output | | |
| 6 | VS+ | Positive remote sense | | |

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Packaging Details

The 9-36V ProLynx[™] modules are supplied in tape & reel as standard. Modules are shipped in quantities of 250 modules per reel.

All Dimensions are in millimeters and (in inches).



Reel Dimensions:

 Outside Dimensions:
 330.2 mm (13.00)

 Inside Dimensions:
 177.8 mm (7.00")

 Tape Width:
 44.00 mm (1.732")

GE

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Surface Mount Information

Pick and Place

The 9-36V ProLynx[™] modules use an open frame construction and are designed for a fully automated assembly process. The modules are fitted with a label designed to provide a large surface area for pick and place operations. The label meets all the requirements for surface mount processing, as well as safety standards, and is able to withstand reflow temperatures of up to 300°C. The label also carries product information such as product code, serial number and the location of manufacture.

Nozzle Recommendations

The module weight has been kept to a minimum by using open frame construction. Variables such as nozzle size, tip style, vacuum pressure and placement speed should be considered to optimize this process. The minimum recommended inside nozzle diameter for reliable operation is 3mm. The maximum nozzle outer diameter, which will safely fit within the allowable component spacing, is 7 mm.

Bottom Side / First Side Assembly

This module is not recommended for assembly on the bottom side of a customer board. If such an assembly is attempted, components may fall off the module during the second reflow process.

Lead Free Soldering

The 9-36V ProLynx[™] modules are lead-free (Pb-free) and RoHS compliant and fully compatible in a Pb-free soldering process. Failure to observe the instructions below may result in the failure of or cause damage to the modules and can adversely affect long-term reliability.

Pb-free Reflow Profile

Power Systems will comply with J-STD-020 Rev. C (Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices) for both Pb-free solder profiles and MSL classification procedures. This standard provides a recommended forced-air-convection reflow profile based on the volume and thickness of the package (table 4-2). The suggested Pb-free solder paste is Sn/Ag/Cu (SAC). The recommended linear reflow profile using Sn/Ag/Cu solder is shown in Fig. 41. Soldering outside of the recommended profile requires testing to verify results and performance.

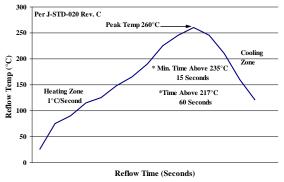
For questions regarding Land grid array(LGA) soldering, solder volume; please contact GE for special manufacturing process instructions.

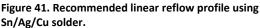
MSL Rating

The 9-36V ProLynx[™] modules have a MSL rating of 2a.

Storage and Handling

The recommended storage environment and handling procedures for moisture-sensitive surface mount packages is detailed in J-STD-033 Rev. A (Handling, Packing, Shipping and Use of Moisture/Reflow Sensitive Surface Mount Devices). Moisture barrier bags (MBB) with desiccant are required for MSL ratings of 2 or greater. These sealed packages should not be broken until time of use. Once the original package is broken, the floor life of the product at conditions of \leq 30°C and 60% relative humidity varies according to the MSL rating (see J-STD-033A). The shelf life for dry packed SMT packages will be a minimum of 12 months from the bag seal date, when stored at the following conditions: $< 40^{\circ}$ C, < 90% relative humidity.





Post Solder Cleaning and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical board testing. The result of inadequate cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning and drying procedures, refer to *Board Mounted Power Modules: Soldering and Cleaning* Application Note (AN04-001).

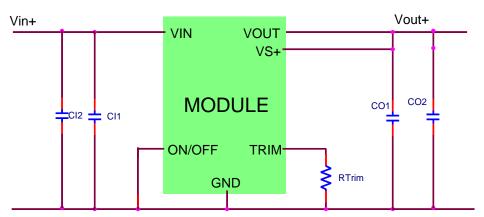
GE

9-36V ProLynx[™] 3A: Non-Isolated DC-DC Power Modules

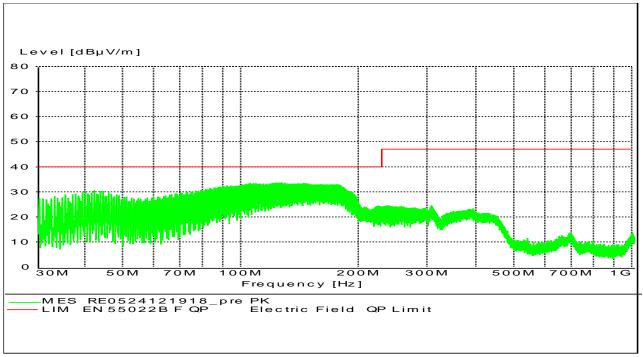
9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

EMC Considerations

The circuit and plots in Figure 42 shows a suggested configuration to meet the radiated emission limits of FCC Class A. Actual performance depends on layout and external components used.



| CI1 | 1 x 0.01µF/50V, 0603 ceramic capacitor |
|-------|--|
| CI2 | 2 x 10μF/50V ceramic capacitor (e.g. Murata GRM32ER71H106K) |
| CO1 | 1 x 0.01µF/25V, 0306 ceramic capacitor (e.g. Murata LLL185R71E103MA01L)) |
| CO2 | 2 x 10μF/25V ceramic capacitor (e.g. Murata GCM32ER71E106KA42) |
| RTrim | 6.19KΩ resistor |



EUT: APXW003A0X3-SRZ / TEM Cell

Fig 42 – EMI Plot of APXW003 on evaluation board with 12 V in @ 1.2 A in / 5 Vdc @ 2.5 A out

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

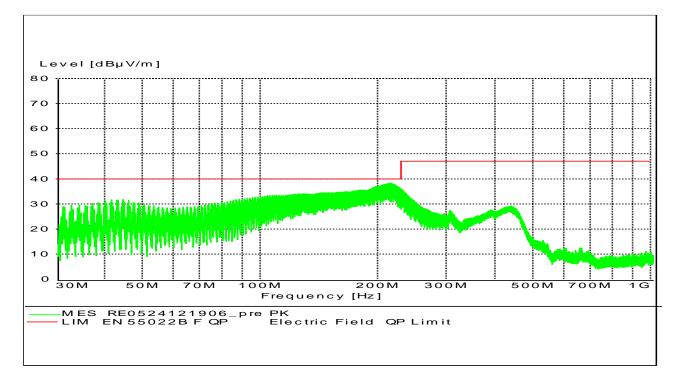


Fig 43 – EMI Plot of APXW003 on evaluation board with 24 V in @ 0.6 A in / 5 Vdc @ 2.5 A ou

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Negative Output Operation

Basic Scheme

The 9-36V ProLynx[™] modules can also be used to create negative output voltages from a positive input voltage. Changing the input connection to as shown in Figure 42 converts the module from a synchronous buck converter to a synchronous flyback converter

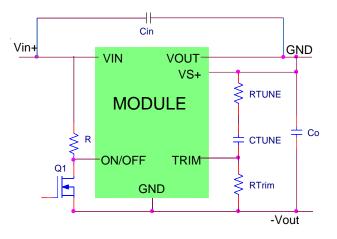


Figure 44. Schematic Connection of 5A ProLynx module for negative output applications.

Remote Enable

Figures 33 and 34 still apply for remote On/Off operation. However the On/Off threshold is now with respect to –Vout instead of Ground. Before the module turns on, output is zero so GND and –Vout are at the same potential. After the modules turns on, -Vout moves down and so will the ON/OFF threshold. The following level shifting circuit can be used in applications to limit exposure of the negative output voltage to the On/Off circuitry.

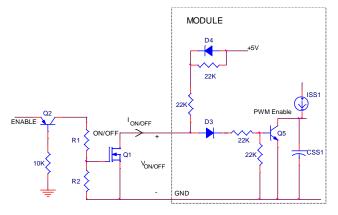


Figure 45. On/Off Level Shifting Circuit for the 3A ProLynx module for negative output applications.

Instead of directly turning Q1 On, the level shifting circuit is used to turn Q1 On by first turning Q2 ON. When Q1 is in the ON state, the ON/OFF pin is pulled low causing transistor Q5 to be OFF and the module to be turned ON. Both Q1 and Q2 are external transistors. To turn the module OFF, Q2 is turned OFF, which turns Q1 OFF causing the ON/OFF pin to be pulled high turning Q5 ON and the module to be turned OFF. If the On/Off pin is left floating, the module will be in the OFF state.

Input Voltage Range

The 9-36V ProLynx[™] modules when connected in a negative output application will support a maximum input voltage which is also a function of the output voltage. The sum of the applied input voltage and magnitude of the output voltage cannot exceed 36V. Vin(applied) +|Vout| ≤ 36 and Vout≤-3V. For e.g. with a -12V output system the max input voltage that can applied is only 24V. However, Figure 35 showing variation of output voltage with Input Voltage should still be considered for determining the required minimum input voltage.

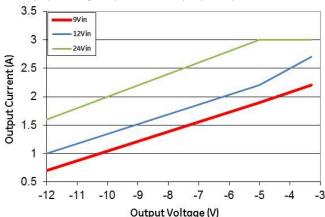
Input voltage turn-on threshold remains the same as the positive output connection. However the input turn-off threshold tracks the output voltage and is reduced by the same level. The listed input Turn-Off threshold of 7.35V when applied to a -3.3Vout application will be measured as a 4.05V (7.35V – 3.3V) threshold. Operating at input voltages below 9V may cause the module to shut down earlier due to OCP inception

Output Voltage Range

The 9-36V ProLynx[™] modules will support the values of trim resistors indicated in Table 1 to generate the same output voltage, except with sign inversion

Overcurrent Protection

The 9-36V ProLynx modules will automatic scale current limit with output voltage through an inverse relationship of the current limit threshold even in negative output voltage mode. This feature is shown graphically in Fig. 34, allows higher output currents to be drawn from the module at lower output voltages thereby optimizing the power delivery capability of the module.



9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Figure 46. Graph showing maximum output current capability at different output voltages.

Efficiency

9-36V ProLynx modules in a negative output application

-3.3Vout and at 25°C

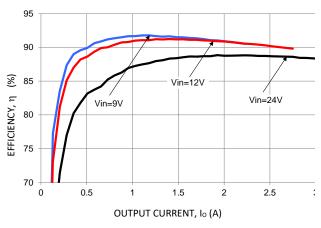


Figure 47. Converter Efficiency versus Output Current.

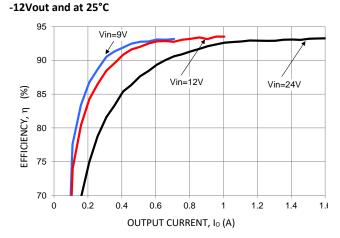


Figure 49. Converter Efficiency versus Output Current.

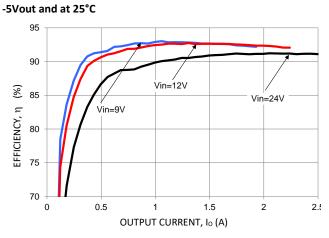


Figure 48. Converter Efficiency versus Output Current.

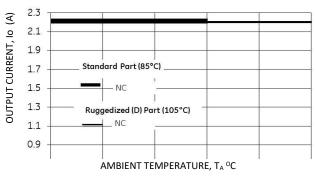
GE

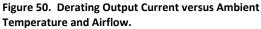
9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Thermal

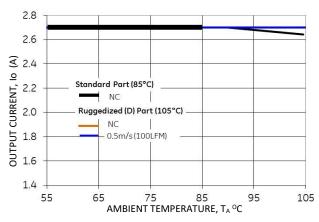
Thermal Derating curves for some of the output voltage settings when the 9-36V ProLynx modules are connected in a negative output application. De-rating curves for -3.3Vout, -5Vout, -12Vout and -18Vout have been provided for input voltages of 9Vin, 12Vin and 24Vin. Intermediate voltages can be estimated through extrapolation of provided data

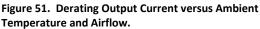
9Vin, -3.3Vout











24Vin, -3.3Vout

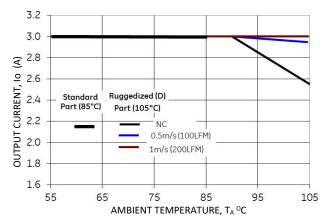


Figure 52. Derating Output Current versus Ambient Temperature and Airflow.

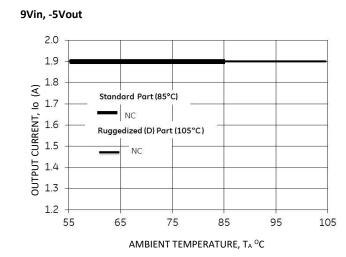


Figure 53. Derating Output Current versus Ambient Temperature and Airflow.

12Vin, -5Vout

9Vdc -36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc -24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

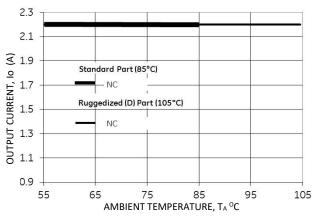


Figure 54. Derating Output Current versus Ambient Temperature and Airflow.



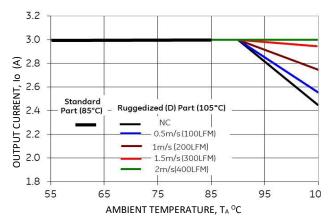
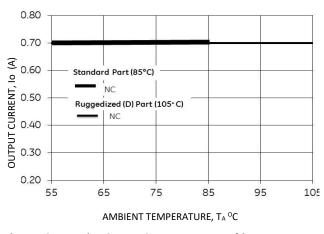
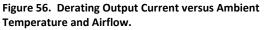


Figure 55. Derating Output Current versus Ambient Temperature and Airflow.







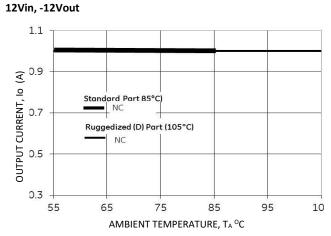
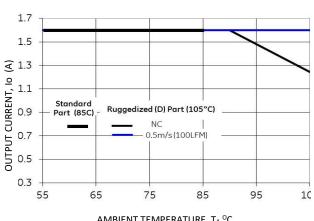


Figure 57. Derating Output Current versus Ambient Temperature and Airflow.



AMBIENT TEMPERATURE, TA ^OC

GE

24Vin, -12Vout

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Figure 58. Derating Output Current versus Ambient Temperature and Airflow.

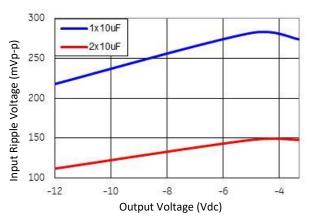


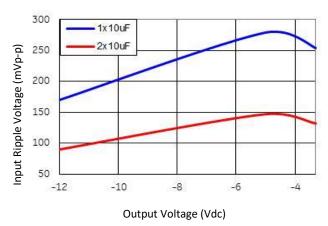
Figure 60. Input ripple voltage with 1x10 μF or 2x10 μF ceramic capacitors at the input (max load).

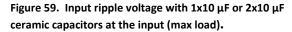
Input Ripple

Input ripple curves have been provided for input voltages of 9Vin, 12Vin and 24Vin.

Ripple at intermediate input voltages can be estimated through extrapolation of provided curves

9Vin





12Vin

24Vin

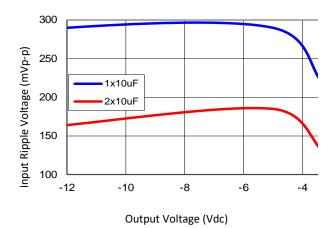


Figure 61. Input ripple voltage with 1x10 μF or 2x10 μF input ceramic capacitors (max load).

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

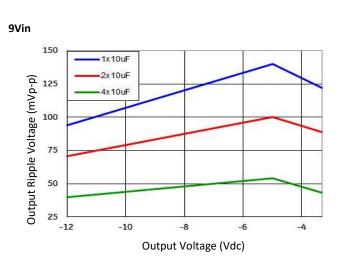


Figure 62. Output ripple with 1x10µF, 2x10µF & 4x10µF output ceramic capacitors (max load).

Output Ripple

Output ripple curves for input voltages of 9Vin, 12Vin and 24Vin Ripple at intermediate input voltages can be estimated through extrapolation. Output Voltage is also roughly proportional to load current level.

Table 7. Peak to Peak Ripple in mV with a 10uF external capacitor at different load levels

| -3.3Vout | | | | | | | | |
|----------|--------|--------------|----------|--|--|--|--|--|
| | 0.1A | 50%Load | 100%Load | | | | | |
| 9Vin | 25 | 63 (1.1A) | 122 | | | | | |
| 12Vin | 27 | 65 (1.35A) | 124 | | | | | |
| 24Vin | 32 | 52 (1.5A) | 87 | | | | | |
| | -5Vout | | | | | | | |
| | 0.1A | 50%Load | 100%Load | | | | | |
| 9Vin | 29 | 74 (0.95A) | 140 | | | | | |
| 12Vin | 34 | 70 (1.1A) | 135 | | | | | |
| 24Vin | 42 | 74 (1.5A) 12 | | | | | | |
| | -12 | Vout | | | | | | |
| | 0.1A | 50%Load | 100%Load | | | | | |
| 9Vin | 40 | 58 (0.35A) | 94 | | | | | |
| 12Vin | 48 | 77 (0.5A) | 125 | | | | | |
| 24Vin | 83 | 111 (0.8A) | 151 | | | | | |

12Vin

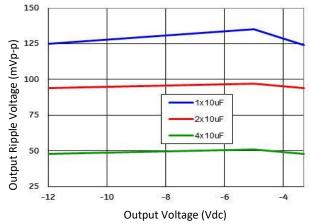


Figure 63. Output ripple with 1x10µF, 2x10µF & 4x10µF output ceramic capacitors (max load).

24Vin

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

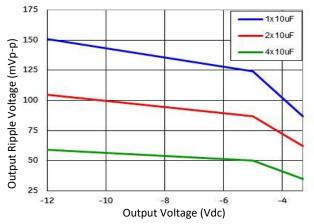


Figure 64. Output ripple with 1x10µF, 2x10µF & 4x10µF output ceramic capacitors (max load).

GE

9-36V ProLynx[™] 3A: Non-Isolated DC-DC Power Modules

9Vdc –36Vdc input; 3Vdc to 18Vdc output; 3A to 1.5A Scaled output current 9Vdc –24Vdc input; -3.3Vdc to -12Vdc output; 3A to 0.7A Scaled output current

Ordering Information

Please contact your GE Sales Representative for pricing, availability and optional features.

Table 8. Device Codes

| Device Code | Input Voltage Range | Output Voltage | Output Current | On/Off Logic | Connector Type | Comcodes |
|----------------------|---------------------------------------|--|--------------------------------|-----------------|-------------------|-------------|
| APXW003A0X3-SRZ | 9 – 36Vdc | 3 – 18Vdc or -3.3 to -12Vdc in negative output application | 3A – 1.5A | Negative | SMT | CC109161238 |
| APXW003A0X43- SRZ | or 9 – 24Vdc in negative output | | or 3A – 0.7A in negative | Positive | SMT | CC109161246 |
| APXW003A0X3- SRDZ | application | | output application | Negative | SMT | 150026706 |

-Z refers to RoHS compliant parts

Table 9. Coding Scheme

| TLynx family | Sequencing feature. | Input voltage range | Output current | Output voltage | On/Off logic | Remote Sense | Options | | ROHS Compliance |
|-----------------|------------------------|---------------------------|-------------------|------------------------|--|-----------------|--|---|--------------------|
| АР | х | W | 003 | Х | 4 | 3 | -SR | -D | Z |
| | X = w/o Seq. | W = 9- 36V | ЗА | programmable output | 4 = positive No entry = negative | Available | S = Surface Mount R = Tape & Reel | D = 105°C operating ambient, 40G operating shock as per MIL Std 810G | Z = ROHS6 |

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